Appl. No. 10/709,502
Amdt. dated April 17, 2006
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IDC-A5,AMD,M

modules according to the process and the settings of ~~other~~ parameters.

~~Please replace Paragraph [0017] with the following amended paragraph:~~

IDC-A6,AMD

[0017] Please refer to FIG. 1. FIG. 1 shows ~~the flow chart of the generation~~ a flow chart of generation of command files of DRC rules according to a preferred embodiment of the present invention.

~~Please replace Paragraph [0025] with the following amended paragraph:~~

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IDC-A7,AMD

[0025] The same procedures can be applied to generate a command file of LVS/LPE rules. FIG. 2 shows ~~the flow chart of the generation~~ a flow chart of generation of command files of LVS/LPE rules according to a preferred embodiment of the present invention.

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~~Please replace Paragraph [0035] with the following amended paragraph:~~

IDC-A8,AMD

[0035] As described above, different modules of program codes are needed when generating different kind of command files. If all the five aforementioned modules of program codes are provided, the claimed method is capable of generating both command files of DRC rules and command files of LVS/LPE rules. Please refer to FIG. 3. Shown in FIG. 3 is a flow chart of ~~the~~ generation of command files of DRC or/and LVS/LPE rules according to a preferred embodiment of the present invention.

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